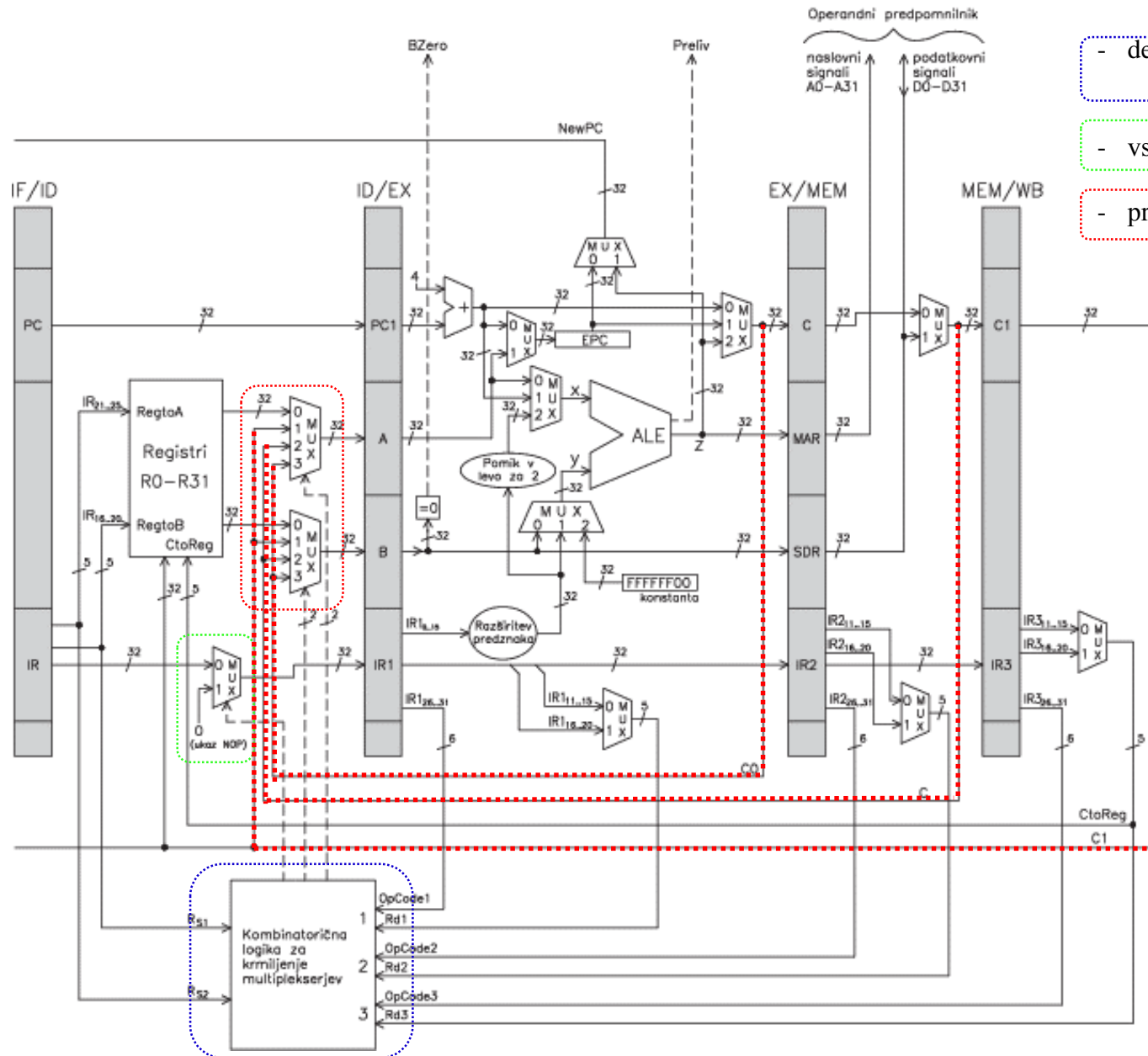


HIP – shema z logiko za podatkovne nevarnosti in premoščanje

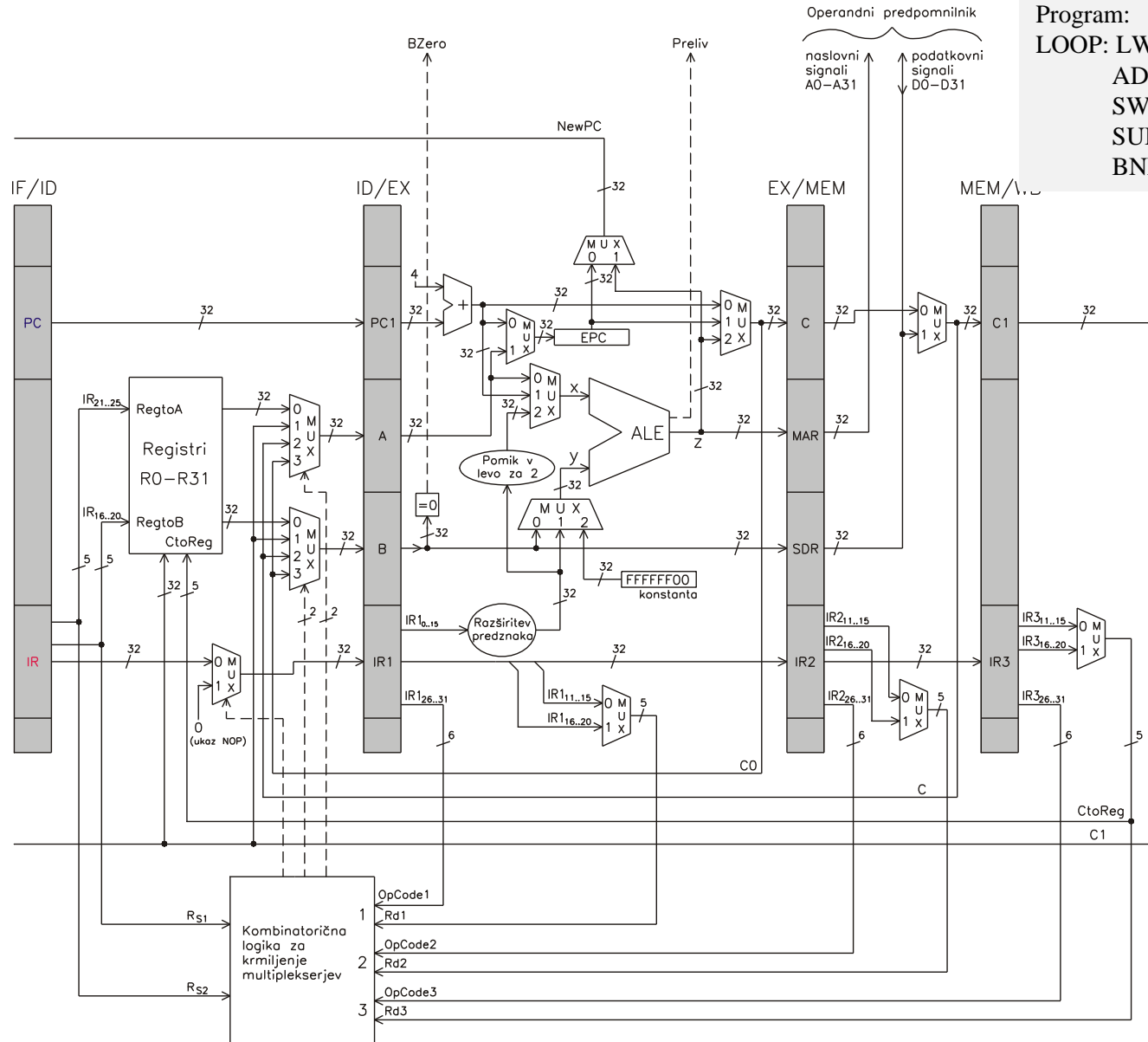


- detekcija nevarnosti
- primerjalnik
- vstavljanje mehurčkov
- premoščanje

Perioda 1

BNE R4, LOOP

HIP - vsa možna premoščanja



```

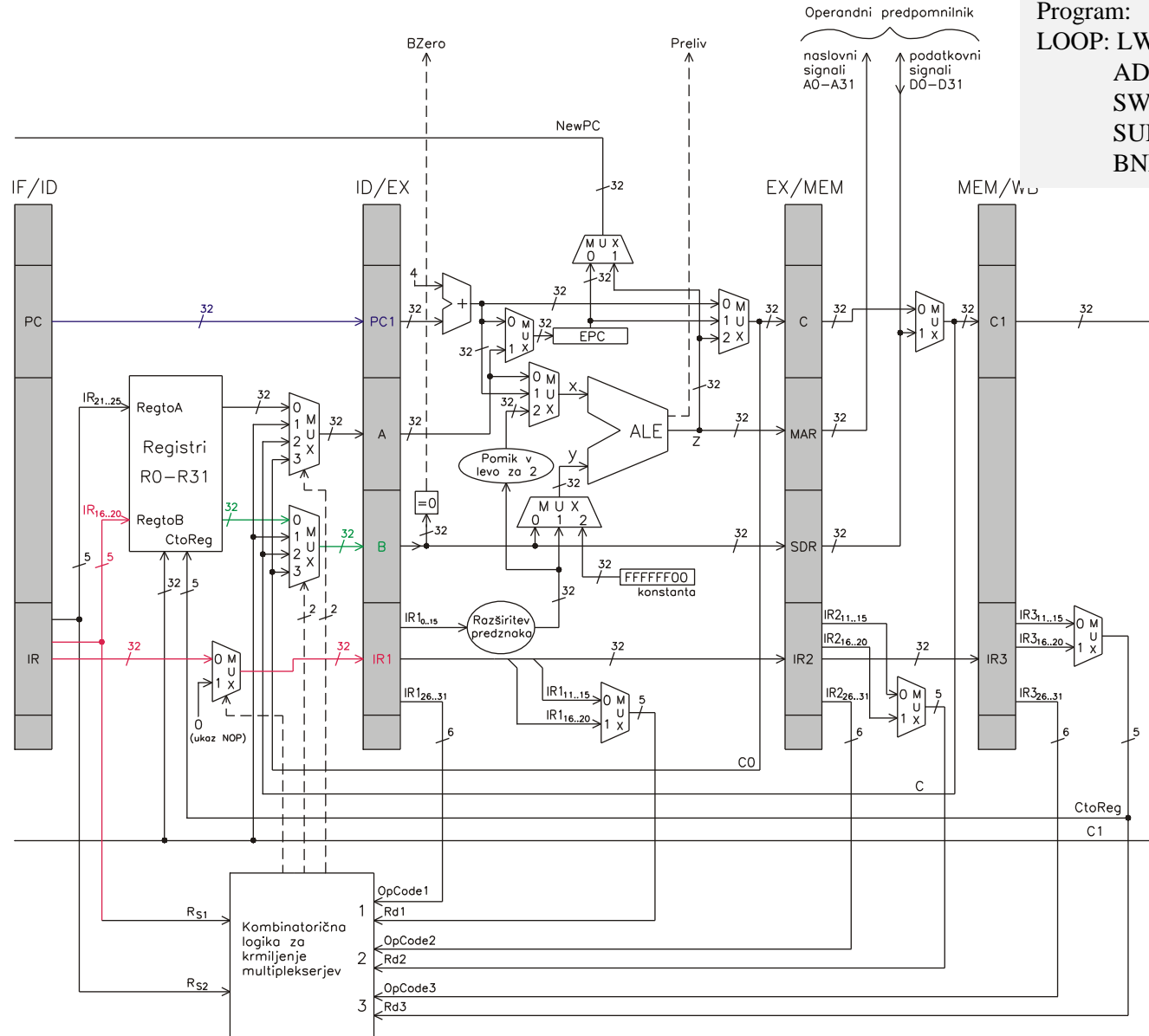
Program:
LOOP: LW    R1, 0(R2)
        ADDI R1, R1, #1
        SW   0(R2), R1
        SUB  R4, R3, R2
        BNE R4, LOOP
    
```

Perioda 2

BNE R4, LOOP

HIP - vsa možna premoščanja

--- --- ---



Program:

```

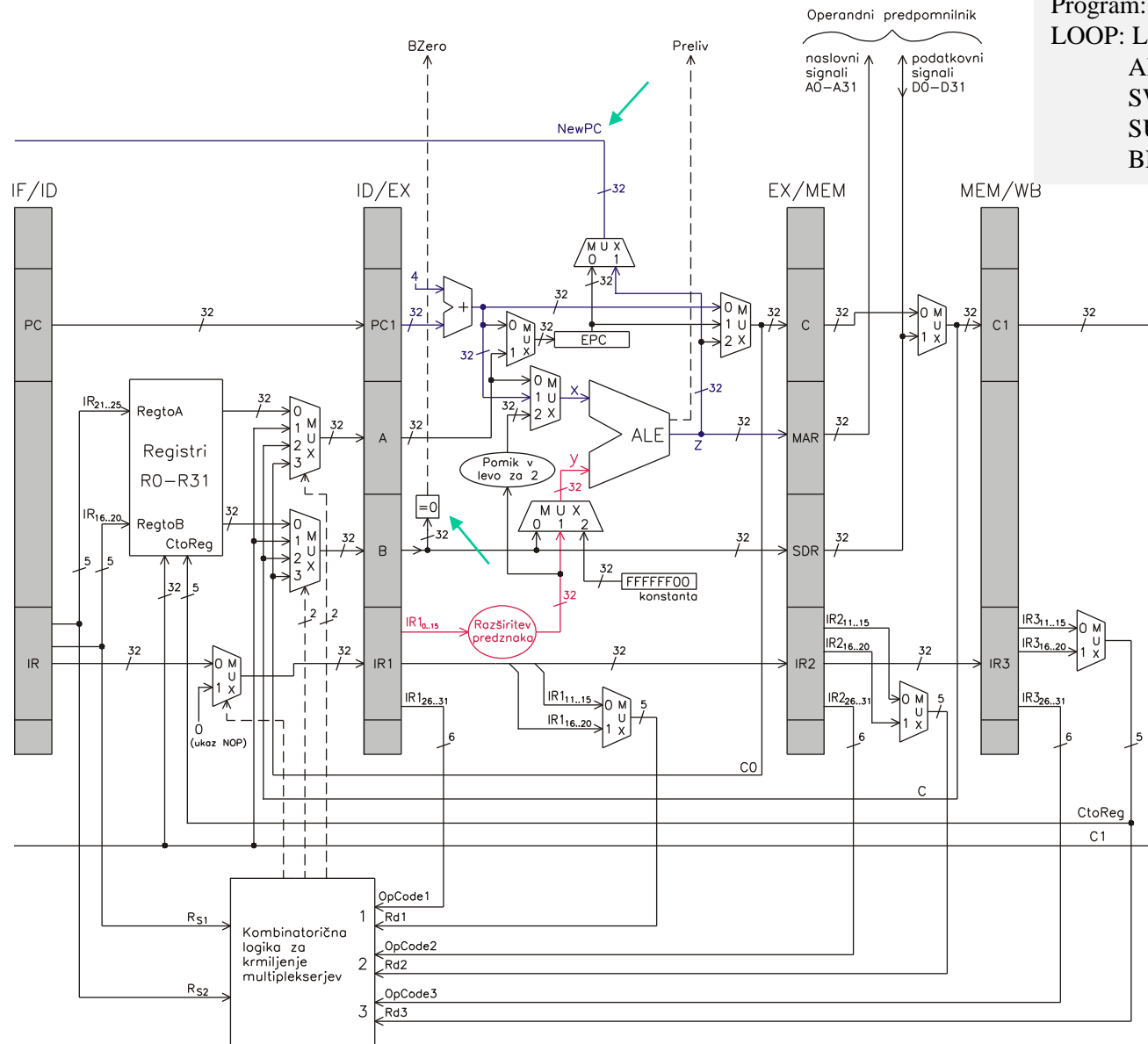
LOOP: LW    R1, 0(R2)
      ADDI  R1, R1, #1
      SW    0(R2), R1
      SUB   R4, R3, R2
      BNE  R4, LOOP
    
```

Operandni predpomnilnik
naslovni signali A0-A31
podatkovni signali D0-D31

Perioda 3

BNE R4, LOOP

HIP - vsa možna premoščanja



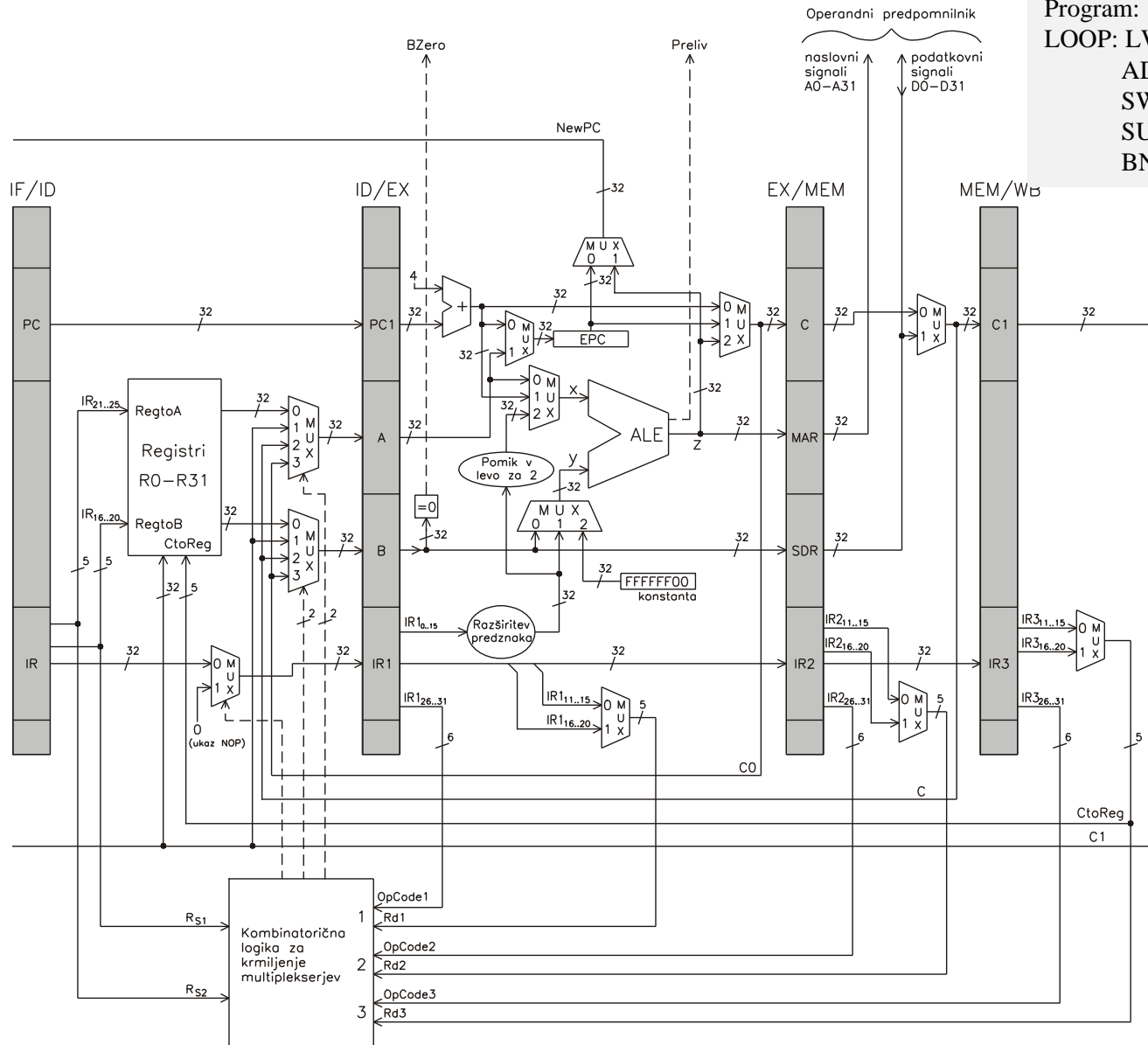
Program:
 LOOP: LW R1, 0(R2)
 ADDI R1, R1, #1
 SW 0(R2), R1
 SUB R4, R3, R2
 BNE R4, LOOP

Perioda 4

LW R1,0(R2)

HIP - vsa možna premoščanja

BNE R4,LOOP



Program:

LOOP: LW R1, 0(R2)
 ADDI R1, R1, #1
 SW 0(R2), R1
 SUB R4, R3, R2
 BNE R4, LOOP

Operandni predpomnilnik

naslovnih signalov A0-A31
 podatkovnih signalov D0-D31

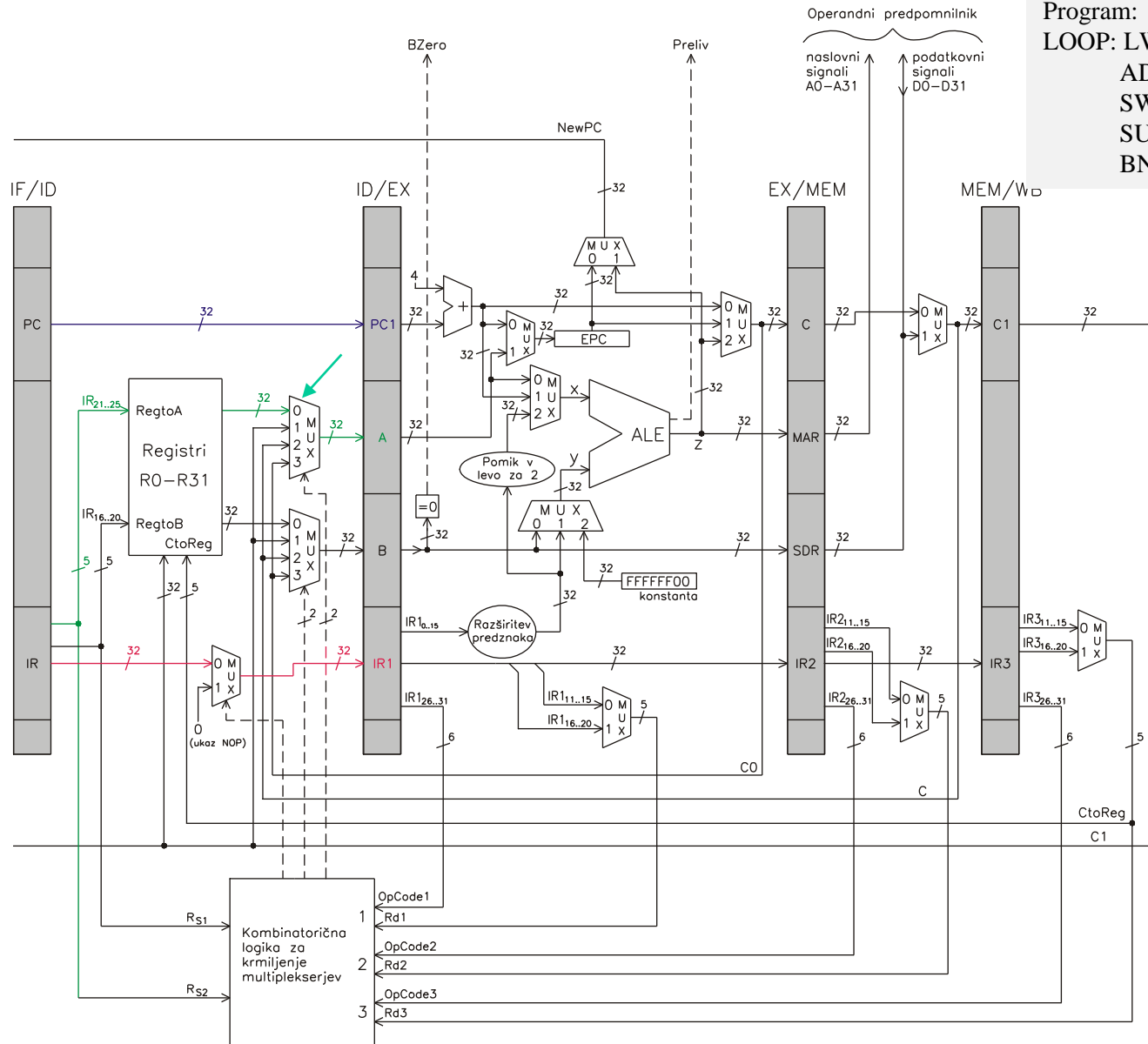
Perioda 5

ADDI R1,R1,# 1

LWR R1,0(R2)

HIP - vsa možna premoščanja

BNE R4,LOOP



Program:

```

LOOP: LW    R1, 0(R2)
      ADDI  R1, R1, #1
      SW    0(R2),R1
      SUB   R4, R3, R2
      BNE  R4, LOOP
    
```

Operandni predpomnilnik

naslovni signali AO-A31
podatkovni signali DO-D31

Kombinatorična logika za krmiljenje multiplexerjev

OpCode1
Rd1
OpCode2
Rd2
OpCode3
Rd3

Perioda 6

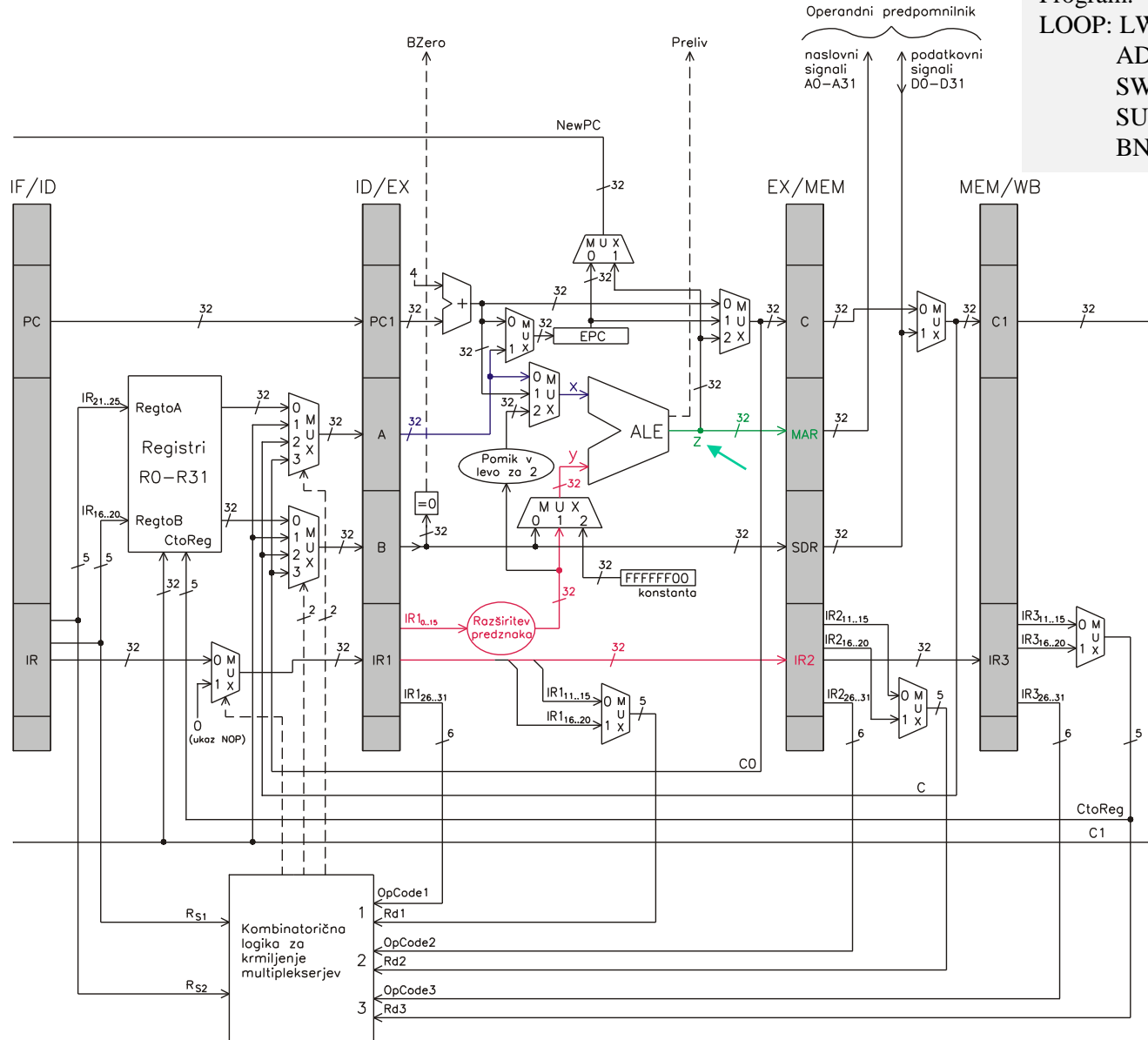
ADDI R1,R1,#1

HIP - vsa možna premoščanja

LWR R1,0(R2)

Program:

LOOP: LW	R1, 0(R2)
ADDI	R1, R1, #1
SW	0(R2),R1
SUB	R4, R3, R2
BNE	R4, LOOP



Perioda 7

SW 0(R2=,R1

ADDI R1,R1,# 1

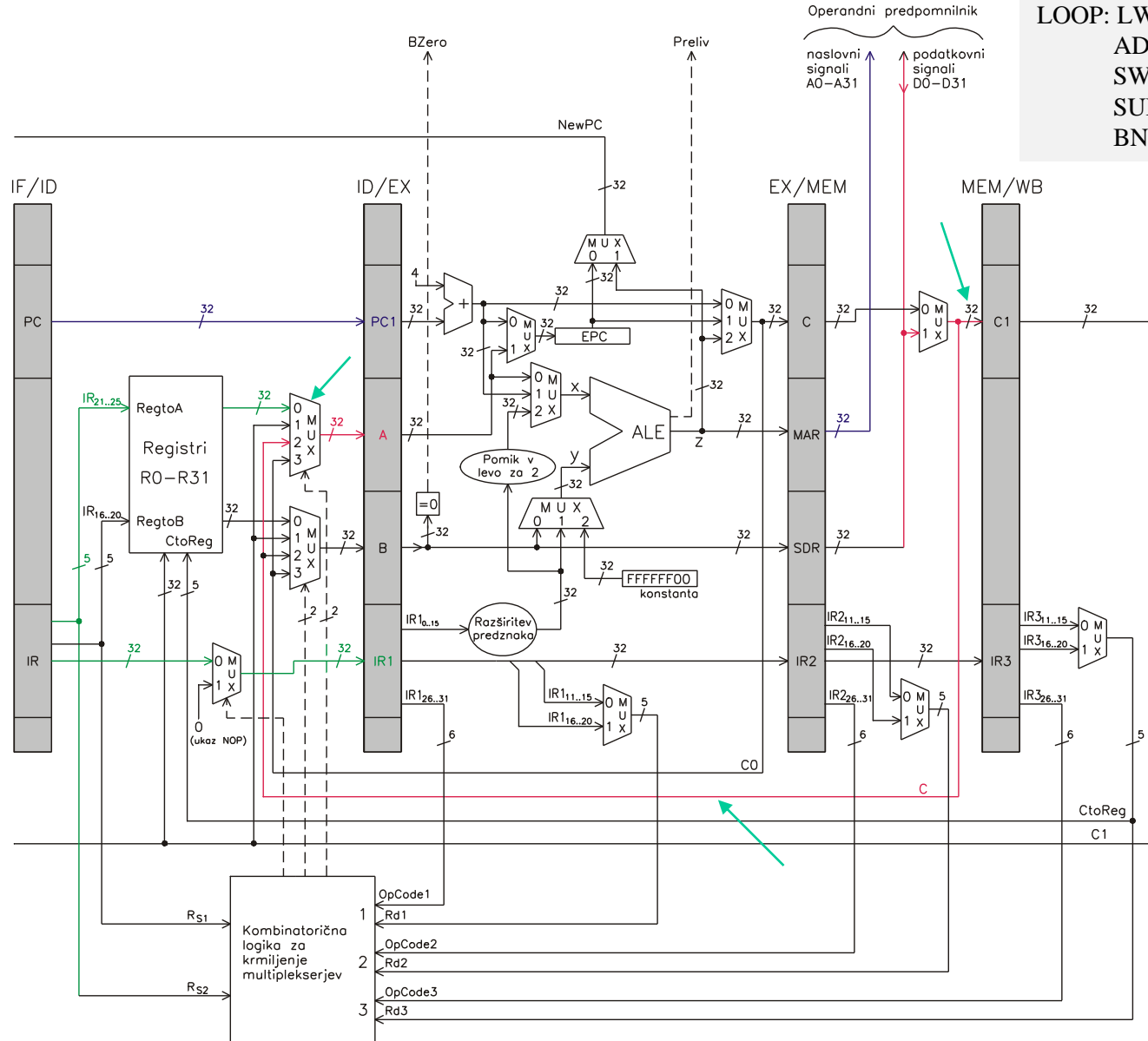
HIP - vsa možna premoščanja

LW R1,0(R2)

Program:

```

LOOP: LW    R1, 0(R2)
      ADDI  R1, R1, #1
      SW    0(R2),R1
      SUB   R4, R3, R2
      BNE  R4, LOOP
    
```



Perioda 8

SUB R4,R3,R2

SW 0(R2),R1

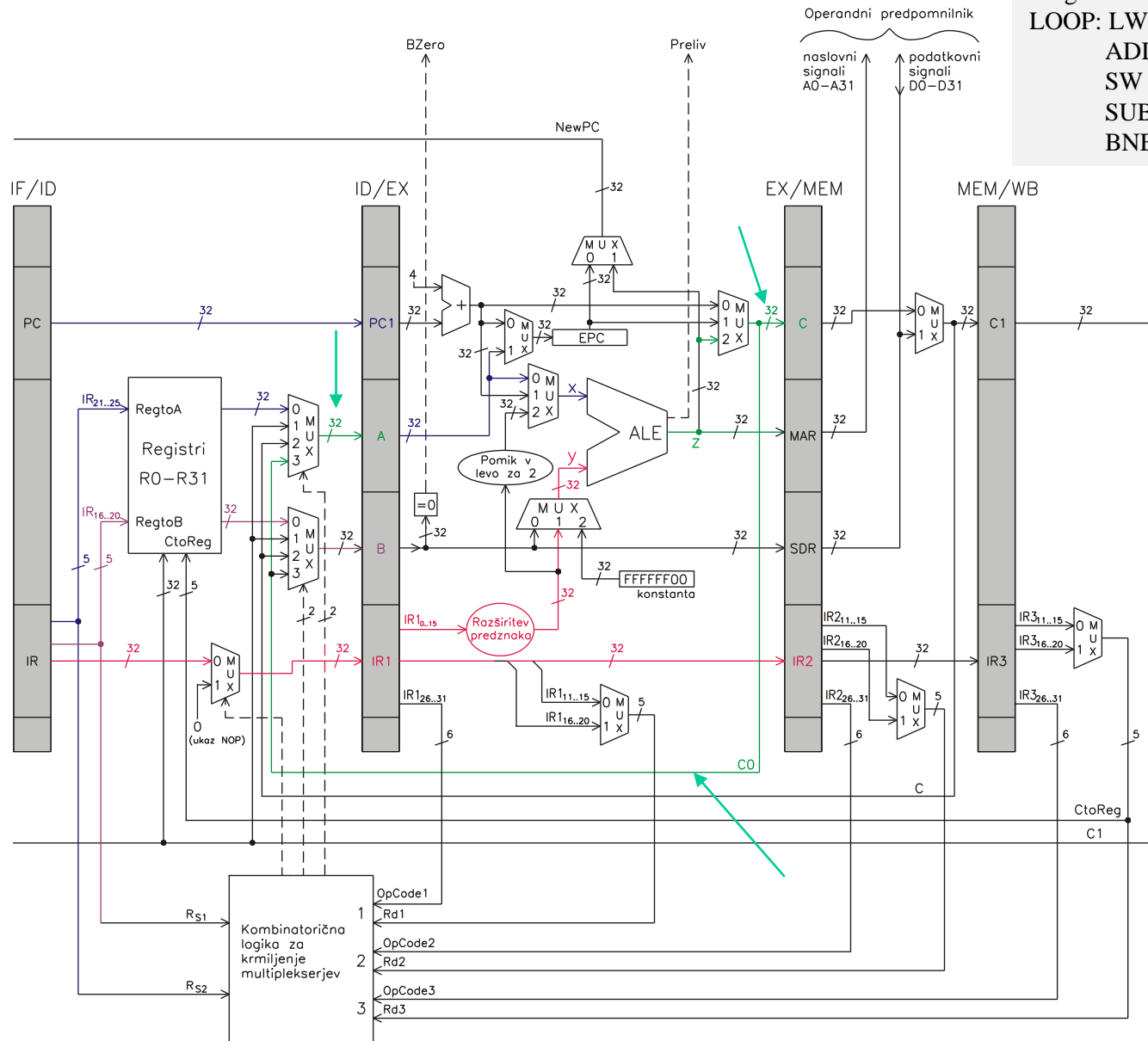
HIP - vsa možna premoščanja

LW R1,0(R2)

Program:

```

LOOP: LW    R1, 0(R2)
      ADDI  R1, R1, #1
      SW    0(R2),R1
      SUB   R4, R3, R2
      BNE  R4, LOOP
    
```



Perioda 9

BNE R4, LOOP

SUB R4, R3, R2

SW 0(R2), R1

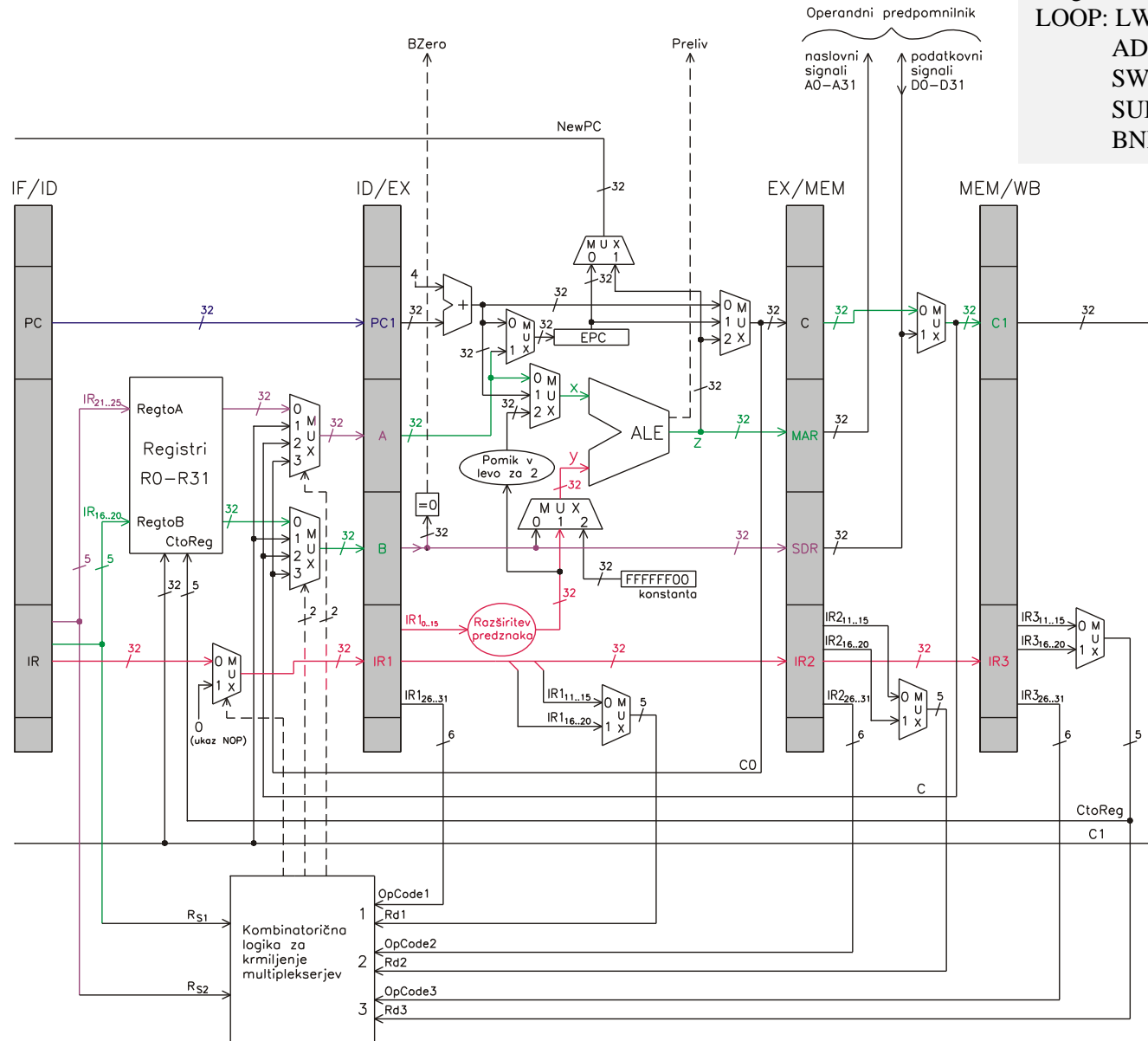
HIP - vsa možna premoščanja

ADDI R1, R1, #1

Program:

```

LOOP: LW    R1, 0(R2)
      ADDI  R1, R1, #1
      SW    0(R2), R1
      SUB   R4, R3, R2
      BNE  R4, LOOP
    
```



Perioda 10

HIP - vsa možna premoščanja

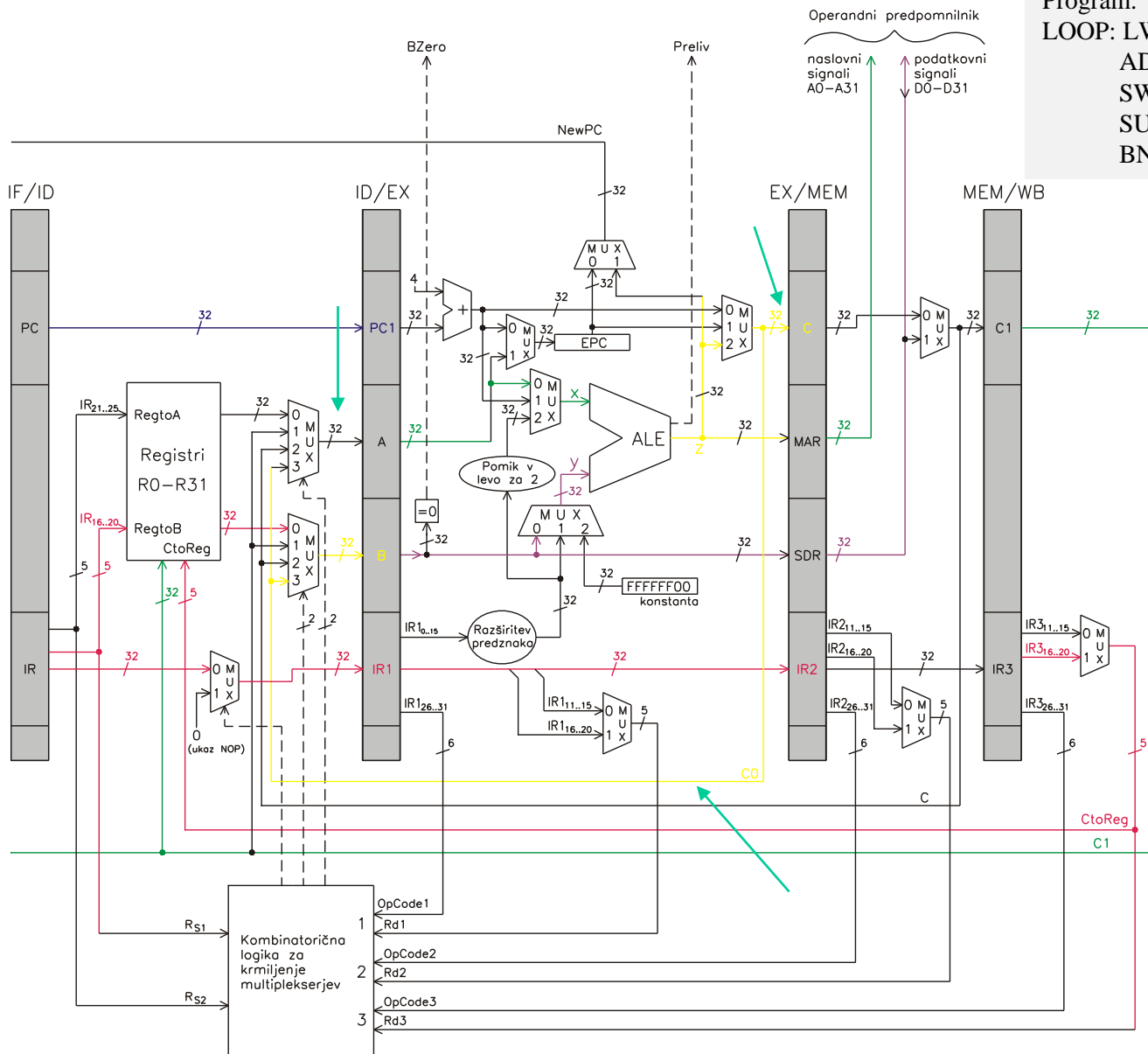
BNE R4, LOOP

SUB R4, R3, R2

SW 0(R2), R1

ADDI R1, R1, # 1

Program:
 LOOP: LW R1, 0(R2)
 ADDI R1, R1, #1
 SW 0(R2), R1
 SUB R4, R3, R2
 BNE R4, LOOP



Perioda 11

HIP - vsa možna premoščanja

BNE R4, LOOP

SUB R4, R3, R2

SW 0(R2), R1

Program:

LOOP: LW	R1, 0(R2)
ADDI	R1, R1, #1
SW	0(R2), R1
SUB	R4, R3, R2
BNE	R4, LOOP

